IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

ATTY DOCKET NO.: 5649-1188

DATE: 03/18/2004



UTILITY PATENT APPLICATION TRANSMITTAL LETTER AND FEE TRANSMITTAL FORM (37 CFR § 1.53(b))

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Şir:
Transmitted herewith for filing under 37 CFR § 1.53(b) is:
a patent application
a Continuation a Divisional a Continuation-in-Part (CIP) Application of prior
application no.:; filed, the disclosure of which is
incorporated herein by reference in its entirety.
and claims the benefit of Provisional Application No; filed
Inventor(s) or Application Identifier:
Jung-hee Chung; Jae-hyoung Choi; Yun-jung Lee; Han-jin Lim
Entitled: INTEGRATED CIRCUIT DEVICES HAVING BARRIER LAYERS BETWEEN UPPER
ELECTRODES AND DIELECTRIC LAYERS AND METHODS OF FABRICATING THE SAME
Myers Bigel Sibley & Sajovec, P.A.
P. O. Box 37428
Raleigh, North Carolina 27627
Telephone: (919) 854-1400
Facsimile: (919) 854-1401
Customer No. 20792
Enclosed are:
1. Application Transmittal Letter and Fee Transmittal Form (A duplicate is enclosed for fee processing)
2. \(\sum 11\) pages of Specification (including 20 claims)
3. S sheets of Drawings (35 USC 113)
4. Oath or Declaration
 a. newly executed (original or copy) b. copy from prior application (37 CFR 1.63(d) (for continuation/divisional) [Note Box 6 Below]
c. DELETION OF INVENTOR(S) (Signed statement deleting inventor(s) named in the prior application)
5. Application Data Sheet. See 37 CFR 1.76
6. Incorporation By Reference (useable if box 4b is checked)
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under
Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated
by reference therein. 7 Microfiche Computer Program (Annualin)
7 Microfiche Computer Program (Annualiu)

9. Applicant clair 10. Information Di 11. Preliminary Ar 12. English Transl 13. Certified Copy 14. Sequence Listi a. computer read b. paper copy c. statement in s 15. An Associate F 16. Return Receipt 17 Deletion of Inv 18. Nonpublication 19. Other: Applica	Support Power of Attorney Postcard (MPEP Pentors(s) (Signed state Postcard (Signed Signed Signe	tus. See 37 CFI at, PTO-1449, are enter all claim ity Document A ing Diskette 503) (Should be seement attached deleting to 5 U.S.C. 122(b) 7 CFR 1.41 (c)	amendment Application pecifically item inventor(s) name in (2)(B)(i). Application	No. 200 nized) n the prior a	to calculate 33-17074; F	Filed 03/19/200	03
The fee has been calculated as shown below: Column 1 Column 2 Small Entity Large Entity							
	Column 1 No. Filed	Column 2 No. Extra	Rate	Fee	Rate	Fee	
BASIC FEE			\$385.00		\$770.00		
TOTAL CLAIMS	20 - 20 =	0	x 09 =		x 18 =		
INDEP CLAIMS	4 - 3 =	1	x 43 =		x 86 = \$8	6.00	
MULTIPLE Dependent Claims Presented			+ 145 =	+ 290 =			
If the difference in Col. 1 is less than zero, Enter "0" in Col. 2			Total		Total = \$856.00		
A check in the amount of the Acheck in the A	ount of is enclose 40.00). Deposit Account N is hereby authorizes communication thing fees under 37	ed to cover the find the second to credit ove to Deposit According to CFR 1.16 for page 2.15.	he amount of rpayments of the property of the	US the for charge (220: f extra contract)	e the follow		

Respectfully submitted,

Elizabeth A. Stanek Registration No. 48,568

CERTIFICATE OF EXPRESS MAILING

Express Mail Label No.: EV381443384US

Date of Deposit: 03/18/2004

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated above and is addressed to: Mail Stop PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Ban Younan

Date of Signature: <u>03/18/2004</u>

ATTORNEY DOCKET NO. 5649-1188

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Jung-hee Chung et al. Application Serial No.: TBA

Filed: CONCURRENTLY HEREWITH

For: INTEGRATED CIRCUIT DEVICES HAVING BARRIER LAYERS BETWEEN UPPER ELECTRODES AND DIELECTRIC LAYERS AND METHODS OF **FABRICATING THE SAME**

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Date: March 18, 2004

APPLICATION FILED UNDER 37 CFR 1.41(c)

Sir:

The above-identified application is being filed on behalf of the inventors, Jung-hee Chung, citizen of the Republic of Korea, residing at 57-5, Cheongnyangni 2-dong, Dongdaemun-gu, Seoul, Republic of Korea; Jae-hyoung Choi, citizen of the Republic of Korea, residing at 108-2404, Hanjin Apt., Gwangmyeong 4-dong, Gwangmyeong-city, Kyungki-do, Republic of Korea; Yun-jung Lee, citizen of the Republic of Korea, residing at 1081-34, Namhyeon-dong, Gwanak-gu, Seoul, Republic of Korea; and Han-jin Lim, citizen of the Republic of Korea, residing at 107-405, Yeoksam Lucky Apt., Dogok 1-dong, Gangnam-gu, Seoul, Republic of Korea, under the provisions of 37 CFR 1.41(c). A Declaration and Power of Attorney from the inventors will follow, 37 CFR 1.63.

Respectfully submitted,

Elizabeth A. Stanek Registration No. 48,568

Myers Bigel Sibley & Sajovec, P.A.

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Ban Younan

Date of Signature: 03/18/2004